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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Jeoung-Mo KOO et al. EXAMINER: Edgardo Ortiz
SERIAL NO.: 10/799,201 GROUP ART UNIT: 2815
FILED: March 12, 2004
FOR: PROGRAMMABLE MEMORY DEVICE, INTEGRATED
CIRCUIT INCLUDING THE PROGRAMMABLE MEMORY
DEVICE, AND METHOD OF FABRICATING SAME

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

This paper is submitted in response to the May 18, 2005 Office Action issued by the United States Patent and Trademark Office in connection with the above-identified application. A response to the May 18, 2005 Office Action is due on June 20, 2005. Therefore, this paper is being timely filed.

CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 13, 2005.

Dated: June 13, 2005


Michael F. Morano

